

QUESTION ONE:

{11*2=22 pts}

- Give 1 assembly statement to define 1 signed word, called **my_var**, having an initial value -55.

`my_var signed word -55`

- Give 1 assembly statement to define a constant, called **daysec**, having a value $24*60*60$.

`daysec = 24*60*60`

- Give 1 assembly statement to define an array **R5** of 1024 signed words containing no initial values.

`R5 signed word dup(?)`

- Give 1 assembly statement to define a buffer **st_buf** that will be used to store a data read from the keyboard using function A of int 21h: a name (20 characters) and a CPR number (9 digits).

`st_buf byte 29 dup(?)`

- The type of the operand used in the instruction `MOV EBX, OFFSET BUF` is:
 - a) Direct
 - b) Immediate
 - ☒ c) Indexed
 - d) Indirect
 - e) None
- The debug command used to execute at once all instructions in the loop body is:
 - a) d Loop
 - ☒ b) p
 - c) r Loop
 - d) p Loop
 - e) None
- The logical address `5F96:5CA4` is converted to physical address:
 - ☒ a) 65604
 - b) BC3A
 - c) 5F96+5CA4
 - d) 5CA40+5F96
 - e) None
- The directive `T5 word 2F, 4 dup(3 dup(6CH, 44), 'A9')` occupies _____ bytes.
 - a) 29
 - ☒ b) 58
 - c) 37
 - d) 74
 - ☒ e) None
- The _____ accepts input files with extension .ASM and produces output files with extension .Lst
 - ☒ a) ASSEMBLER
 - b) DEBUG
 - c) LINKER
 - d) COMPILER
 - e) None
- If register ax contains A4CE, then after executing "ROR ax, 4" register ax will contain:
 - a) 0A4C
 - b) 4CEA
 - ☒ c) EA4C
 - d) CEA4
 - e) None
- If register ax contains A4CE, then after executing SAR ax, 4 register ax will contain:
 - a) 0A4C
 - b) 4CEA
 - c) CEA4
 - ☒ d) FA4C
 - e) None

QUESTION TWO:

{5+5=10 pts}

- Give a sequence of instructions to calculate: $A = (B \times C) \times D$, where B, C, and D are all memory words already initialized with proper signed values, and A is defined as a doubleword.

<pre> mov ax, B imul c mov ax, D imul dx movsx eax, ax mov A, eax </pre>	
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- Give a sequence of instructions that stores in BL register the count of "ZERO" bits in EAX register and stores in BH register the count of "ONE" bits in EAX register. (Use single loop).

<pre> mov BL, 0 mov BH, 0 mov ecx, 32 L1: shl eax, 1 jc L2 inc BL jmb done </pre>	<pre> L2: inc BH done </pre>
---	--------------------------------------

```

mov ecx, 32
mov ebx, 0
mov edx, 0
L1: mov eax, C
imul ebx, eax
mov eax, D
imul edx, eax
mov eax, ebx
imul edx, eax
mov ebx, 0
        
```

QUESTION THREE: Convert the following C++ code into equivalent assembly code

{12 pts}

```
int num, sum = 0;
cout << "Enter a number please: ";
cin << num;
if (num < 0)
    num = -num;
while (num != 0)
{
    sum += num % 10;
    num /= 10;
}
cout << "Sum = " << sum << endl;
```

include <iostream>

using namespace std;

int main()

{

cout << "Enter a number please: ";

int num;

cin >> num;

if (num < 0)

{

num = -num;

}

while (num != 0)

{

int sum = 0;

while (num != 0)

{

sum += num % 10;

num /= 10;

}

}

cout << "Sum = " << sum << endl;

return 0;

}

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QUESTION FOUR:

{4+4+4=12 pts}

- a) Give **NO MORE THAN 4** instructions to subtract $M3 = M3 - M4$, where $M3$ and $M4$ are quadword memory locations (8 bytes each) defined as shown below:
- | | | |
|----|-------|-------------------|
| M3 | Qword | 70F05060D0801020H |
| M4 | Qword | 6020E03090C010F0H |

```
mov eax, [M3]
sub eax, [M4]
```

- b) Give **NO MORE THAN 4** instructions to swap the left half of EAX register with the right half of EBX register and swap the right half of EAX register with the left half of EBX register.

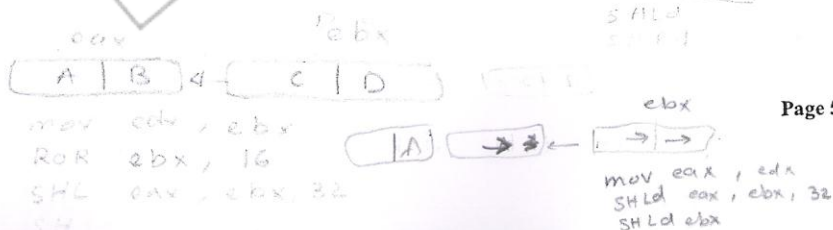
```
mov dx, ebx
SHLD ebx, eax, 2
```

```
mov ecx, 32
L1:
SHL ebx, 1
ROR eax, 1
```

- c) Carefully study the following code and give **ONE** phrase strictly describing what does it do?

```
mov edx, [eax]
ROR ebx, 16
SHLD eax, ebx
```

```
.data
T1 BYTE "ITCS241, Computer, Science, College, of, IT, University, of, Bahrain", 0
.code
MOV EDX, offset T1
XOR EAX, EAX
MOV ECX, lengthof T1
L2: cmp byte ptr [EDX], ' '
JNE L3
INC EAX
MOV byte ptr [EDX], ' '
L3: INC EDX
LOOP L2
CALL WRITEDEC
```



QUESTION FIVE:

{2+2+5+3=12 pts}

- a) Give 1 instruction to store 0 in CX register without using MOV instruction.

```
and cx, 0
```

- b) Give 1 instruction to add the value 2AC9H to the contents of memory word pointed by BX register.

```
add bx, 2ac9h
```

- c) Give NO more than 5 instructions to compute: $EAX = AL * 1024 - BL * 128$. Using MULTIPLY instructions is NOT allowed.

```
movzx eax, al
shl eax, 10
movzx ebx, bl
shl ebx, 7
add eax, ebx
```

- d) Give no more than 3 instructions to divide the predefined 2 signed byte values: BT1 / BT2.

```
mov eax, 1
mov al, BT1
mov ebx, BT2
div ebx
```

```
movzx eax, al
shl eax, 10
movzx ebx, bl
shl ebx, 7
sub ebx, ebx
mov eax, eax
```

QUESTION SIX: USE INTERRUPTS FOR ALL INPUT / OUTPUT OPERATIONS. {12 pts}

Write a procedure **macP8** that accepts a letter passed in **DL** register and displays on the screen the letter itself and its left and right neighboring letters. For example, if you pass 'f', the procedure will display 'efg'.

Write a complete program that: prompts the user to enter from the keyboard a lower-case letter (b...y), validates the input, loops until a valid letter is entered, and calls the developed above procedure **macP8**.

- model small
- stack 100h

- data
prompt byte "Enter lower case letter", 0

- code
mov ax, 0 data
main proc 2x

V: mov AH, 09H
mov DX, offset prompt
int 21H

mov AH, 01H
int 21H

mov AL, 01
mov CX, 26

L:
cmp AL, DL
ja done
inc AL

loop L

done:

call macP8

mov AH, 04H

int 21H

main endp

macP8 proc

push dx

mov AL, DL

mov AH, 02H

mov DL, [AL-1]

int 21H

mov DL, AL
mov AH, 02H
int 21H

mov AL, AL+1
mov AH, 02H
int 21H

ret

macP8 endp

End: main

QUESTION SEVEN:

{2x4=08 pts}

Study carefully the following program and answer all questions below

```

1) INCLUDE I RVINE32.INC
2) SHIFT MACRO VAL, AMT, DIR
3) SH&DIR VAL, AMT
4) ENDM
5) .CODE
6) MAIN PROC
7) XOR EAX, EAX
8) MOV BL, 20H
9) MOV BH, 0
10) MOV AX, BX
11) SHIFT AX, 6, L
12) SHIFT BX, 2, L
13) SUB AX, BX
14) MOVZX EAX, AX
15) CALL WRITEHEX
16) MOV DX, 3F7CH
17) SHIFT DX, 4, R
18) EXIT
19) MAIN ENDP
20) END MAIN
    
```

- The expansion of the macrocall in line 11 is:

SHL AX, 6

- After executing the above program, the value displayed is 0000880 H

- The expansion of the macrocall in line 17 is:

SHR DX, 4

- After executing the above program, the register DX will contain 03F7 H.